

S/N 10/517,943

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

TSUKADA

Examiner:

LEE

Serial No.:

10/517,943

Group Art Unit:

2832

Filed:

December 13, 2004

Docket No.:

10921.0262USWO

Title:

CHIP RESISTOR AND METHOD OF MAKING SAME

CERTIFICATE UNDER 37 CFR 1.6(d):

1 hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on August 21,

2007.

Name: Antonette C Peters

COMMUNICATION REGARDING PRIORITY ACKNOWLEDGEMENT

Mail Stop: ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Dear Commissioner:

Applicant respectfully draws the Examiner's attention to the acknowledgement of foreign priority on the Notice of Allowance for the above referenced application. The applicant believes that box 3.a.2. was checked instead of 3.a.3.

A Supplemental Notice of Allowance is requested acknowledging that copies of the certified copies of the priority document have been received from the International Bureau. If a telephone conference would be helpful in resolving any issues concerning this communication, please contact Applicants' primary attorney-of record, Douglas P. Mueller(Reg. No. 30,300), at (612)455.3804.

Respectfully submitted,

52835

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Dated: August 21, 2007

By: V V Douglas P. Mueller

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AN INTERNATIONAL INTELLECTUAL PROPERTY LAW FIRM

FAX TRANSMISSION

August 21, 2007

TO:

Mail Stop: ISSUE FEE

Examiner: LEE

Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

FROM: Douglas P. Mueller

OUR REF: 10921.0262USWO

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Total pages, including cover letter:

PTO FAX NUMBER: 571.273.2885

If all pages are NOT received, please call us at 612.455.3800 or fax us at 612.455.3801.

Title of Document:

Part B - Fee Transmittal

Communication regarding Priority Acknowledgement

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Name: Douglas P. Mueller

Reg. No.: 30,300

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Antonette C. Peters

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